

Think Silicon is one of the few companies worldwide that have the expertise to develop ultra-low power, high-performance graphic IP technology for mobile/embedded devices. The company is seeking for enthusiastic engineers to join our team and accelerate the development of the next generation of heterogeneous, multicore GPUs targeting the Wearable and IoT market.

## IC Verification Engineer

### Job Description

Within the hardware design group, you will be assigned with the tasks of executing the verification processes of the IP cores both in RTL and FPGA level. You will cooperate closely with hardware and software architects and designers to accomplish your tasks. A successful candidate is expected to undertake the role of planning the verification activities for complex IP blocks using state-of-the-art verification methodologies. Ability to collaborate with customer IC teams to reach successful integration of GPU cores and tape-outs is essential.

### Required Skills:

- Strong background in hardware description languages (Verilog or VHDL).
- Good knowledge of FPGA development.
- Experience with verification methodology such as UVM, OVM or VMM.
- Proficiency in one or more scripting languages (e.g. Python, Tcl, Perl).
- Ability to work pro-actively, creatively, and innovate.
- 2+ years hands on experience in a verification environment.

### Preferred Skills:

- Experience in product life cycle with at least one successful chip tape-out
- Experience with SystemVerilog, SVA and functional coverage
- Deep understanding in formal verification technologies and verification coverage
- Proven skills and ability to analyse and debug RTL design issues
- Experience in design automation
- Demonstrated ability in applying appropriate test methodologies including writing test plans and test cases

### Think Silicon offers:

- Competitive salary
- Flexible work environment
- Space for your own creativity & ideas
- The opportunity to create world-class technology
- Exposure to international customer projects & standard bodies

### Location:

The candidate will work in our offices in Patras or Athens, Greece.

### Contact:

If you feel that you want to contribute to our company, you are welcomed to send your CV to [careers@think-silicon.com](mailto:careers@think-silicon.com) (Reference number: **ICVer0x916**)

#### General:

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